

REMARKS/ARGUMENTS

Claims 1-26 remain canceled without prejudice, claims 27- 47 remain pending. Claims 27, 34, and 41 are amended, as recited hereinabove.

Claims 27-29, 31-33, 41-43, and 45-47 have been rejected under 35 U.S.C. 103(a), as being allegedly unpatentable over Ozcelik (USPN: 5928321, hereinafter referred to as “Ozcelik”) and Bubil (USPN: 6012137, hereinafter referred to as “Bubil”) in view of Chen (USPN: 6473864, hereinafter referred to as “Chen”) and Mahalingaiah (USPN: 5987592, hereinafter referred to as “Mahalingaiah”).

It is believed that claims 1, 34, and 41, are not taught, suggested or hinted at by Ozcelik, Bubil, Chen or Mahalingaiah or a combination thereof. The claimed invention recites “a register group for updating status of registers related to time critical tasks; and another register group ..., wherein the processing unit need access status of registers only in the register group for execution of time-critical tasks thereby avoiding saving and restoring status of the another register group for execution of time-critical tasks”. It is believed that this is not taught by Mahalingaiah.

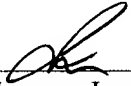
In paragraph 3, page 3 of the Final Office Action, citation to certain text in Mahalingaiah is made as the basis for such teaching but respectfully, such is not the case. In Mahalingaiah, an apparently hierarchical structure of resources is disclosed rather than partitioned resources, such as the claimed invention [See Mahalingaiah; Col. 12, Ins. 4-42]. In Mahalingaiah, the hierarchical includes several register levels. At the top of the hierarchy is a plurality of primary registers. In addition, the top level of the hierarchy includes a special register pointer and a floating point register pointer. Each pointer identifies a second level of the register hierarchy. For example, one of the primary register addresses may be a pointer to a special register hierarchical level and another primary register address may be a pointer to a floating point register hierarchical level. [See Mahalingaiah: Col. 12, Ins. 44-57]. This is hardly the partitioning of the resources of the claimed invention and the register groups recited therein, which are not in hierarchical structure. Furthermore, the primary registers of Mahalingaiah are identified directly in a field of an operand of an instruction. [See

Mahalingaiah; Col. 12, lns. 58-61], which suggests saving and restoring of register status is required, the very notion away from which the claimed invention teaches. Thus, Mahalingaiah does not teach or disclose the claimed register groups of the present invention. The combination of Mahalingaiah with other cited prior art is believed to be in error. As a separate basis of patentability, even if Mahalingaiah did disclose the register groups of the claimed invention, which it does not, the combination thereof with Chen and Ozcelik as the basis of an obviousness rejection is respectfully disagree with because while, in hindsight, it may be obvious to combine these references, at the time of the invention, such combination was not obvious as none of these references suggests the teachings of the others and there is motivation for such combination. Data processing cannot serve as such a motivation because among other reasons, almost all inventions in the area of processing would be rendered obvious, which is not the intention of the law.

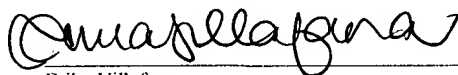
Accordingly, reconsideration and allowance of claims 27-47 is hereby respectfully requested. Applicants submit that the subject application is now in condition for allowance and an early notice thereof is requested. Should any further amendment be required prior to passing the application to issue, the Examiner is respectfully invited to contact the undersigned by telephone at the number set out below.

Respectfully submitted,

Dated: March 23, 2006
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I hereby certify that this correspondence with all attachments is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 by Erika Villafana on March 23, 2006.


Erika Villafana